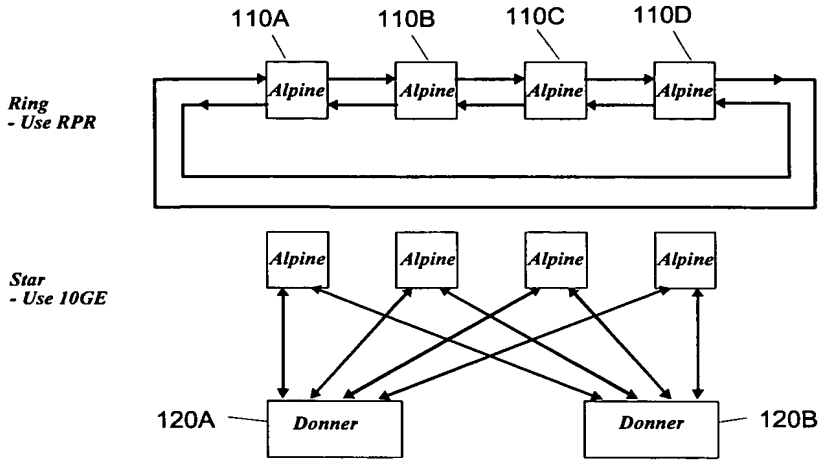


FIGURE 1

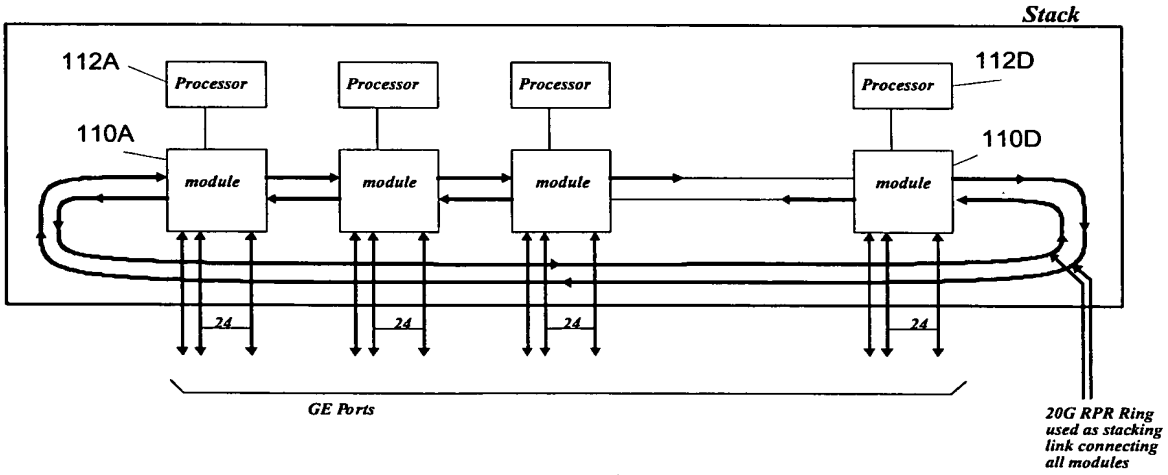


FIGURE 2

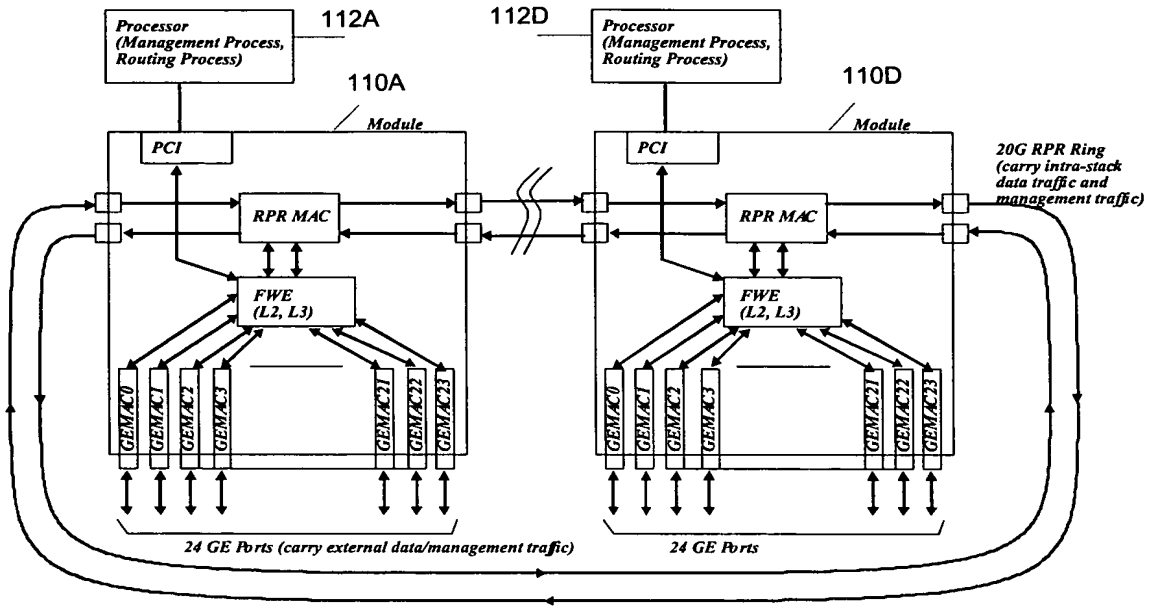

Processor
(Management Process,
Routing Process)
112A
112D
Processor
(Management Process,
Routing Process)
110A
110D
Module
Module
PCI
PCI
20G RPR Ring
(carry intra-stack
data traffic and
management traffic)
RPR MAC
RPR MAC
FWE
(L2, L3)
FWE
(L2, L3)
GEMAC0
GEMAC1
GEMAC2
GEMAC3
GEMAC21
GEMAC22
GEMAC23
GEMAC0
GEMAC1
GEMAC2
GEMAC3
GEMAC21
GEMAC22
GEMAC23
24 GE Ports (carry external data/management traffic)
24 GE Ports

FIGURE 3

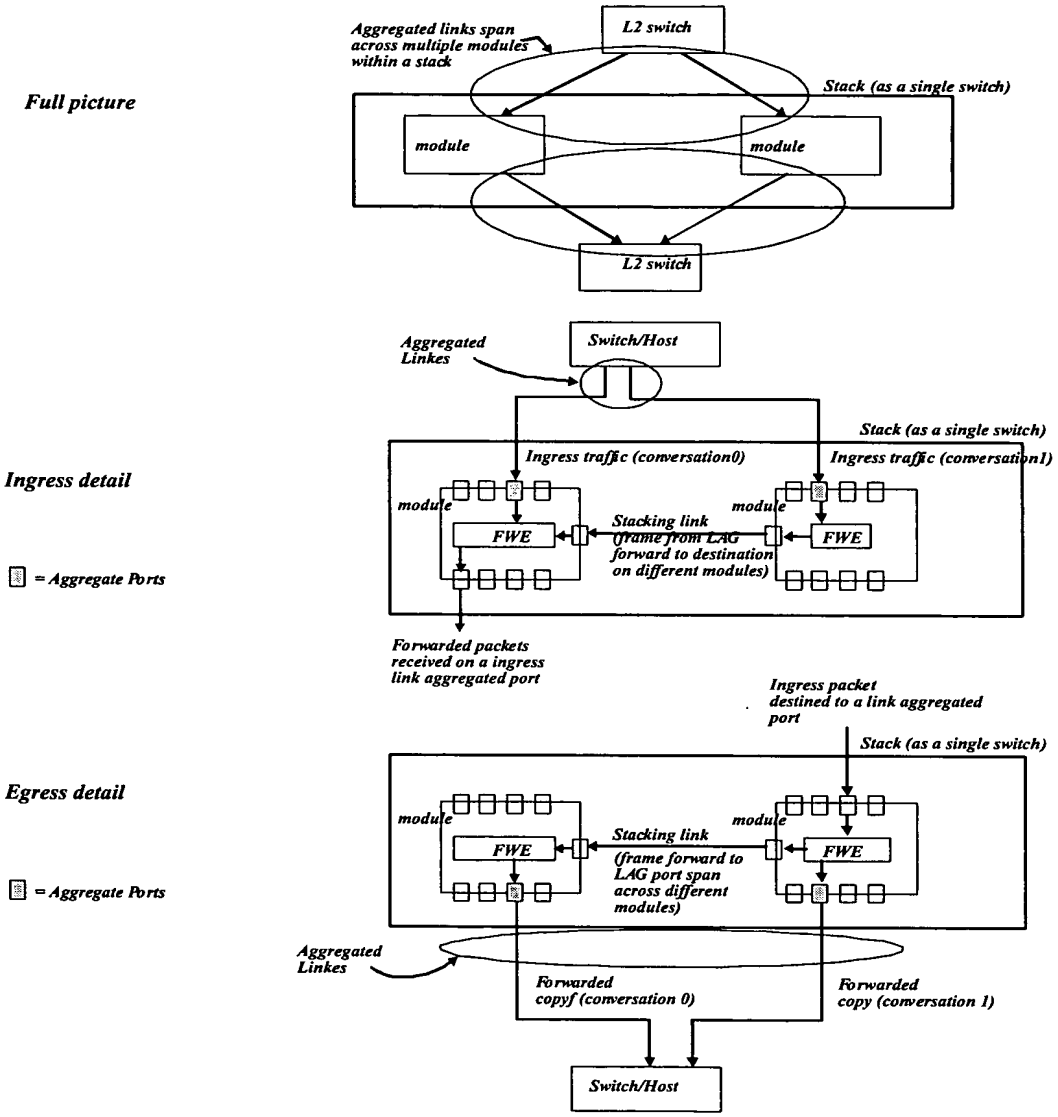

Full picture
Aggregated links span across multiple modules within a stack
L2 switch
Stack (as a single switch)
module
module
L2 switch
Ingress detail
Aggregated Linkes
Switch/Host
Stack (as a single switch)
Ingress traffic (conversation0)
Ingress traffic (conversation1)
module
module
FWE
FWE
Stacking link (frame from LAG forward to destination on different modules)
= Aggregate Ports
Forwarded packets received on a ingress link aggregated port
Ingress packet destined to a link aggregated port
Stack (as a single switch)
Egress detail
module
module
FWE
FWE
Stacking link (frame forward to LAG port span across different modules)
= Aggregate Ports
Aggregated Linkes
Forwarded copyf (conversation 0)
Forwarded copy (conversation 1)
Switch/Host

FIGURE 4

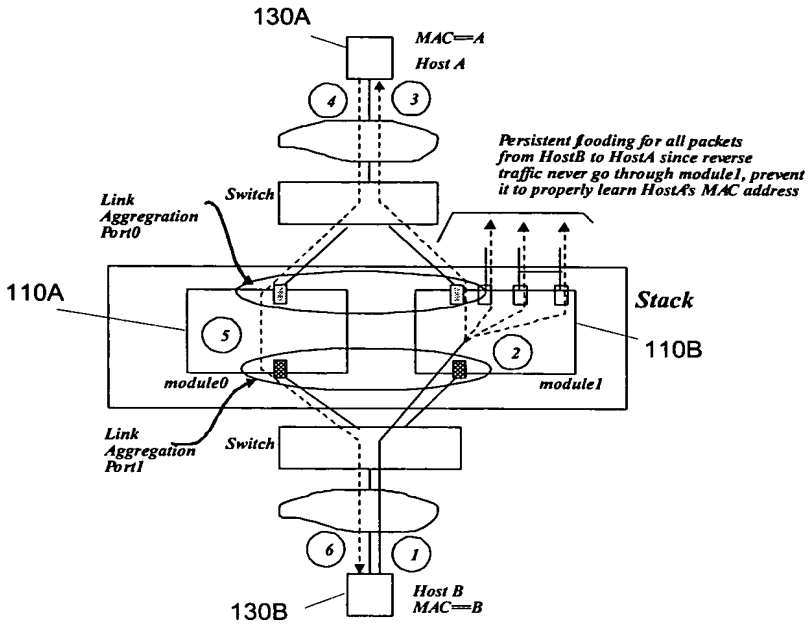


FIGURE 5

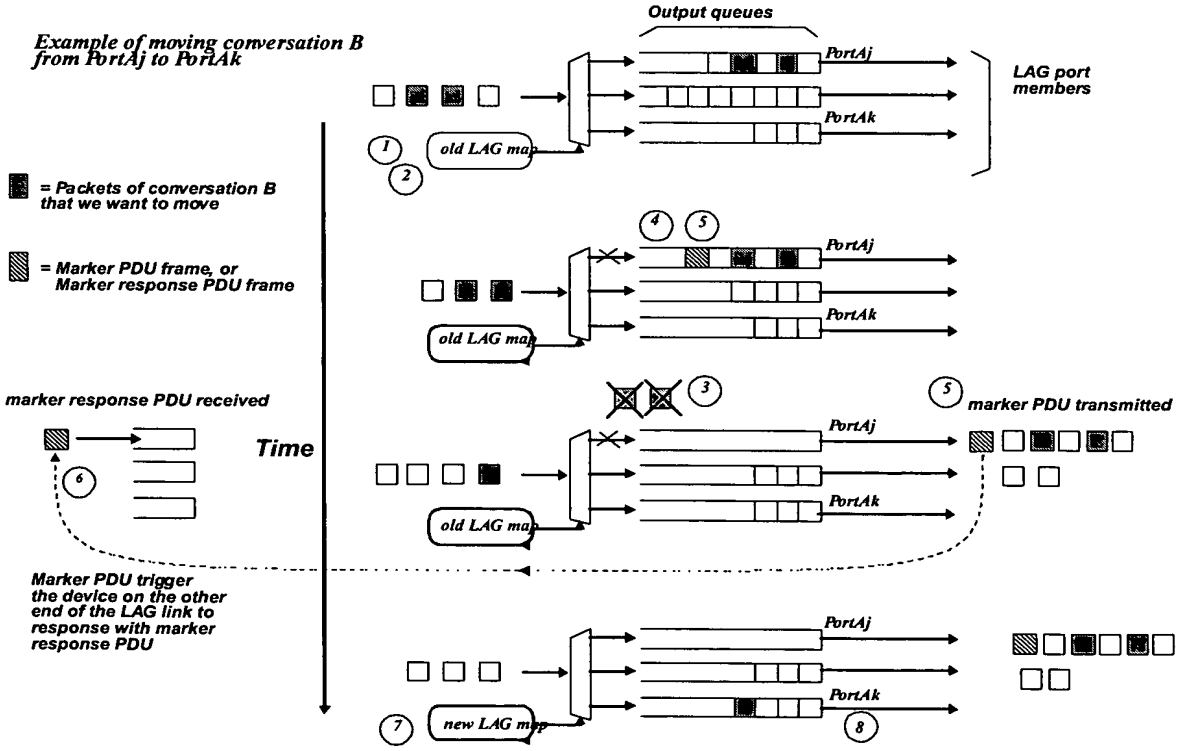


FIGURE 6

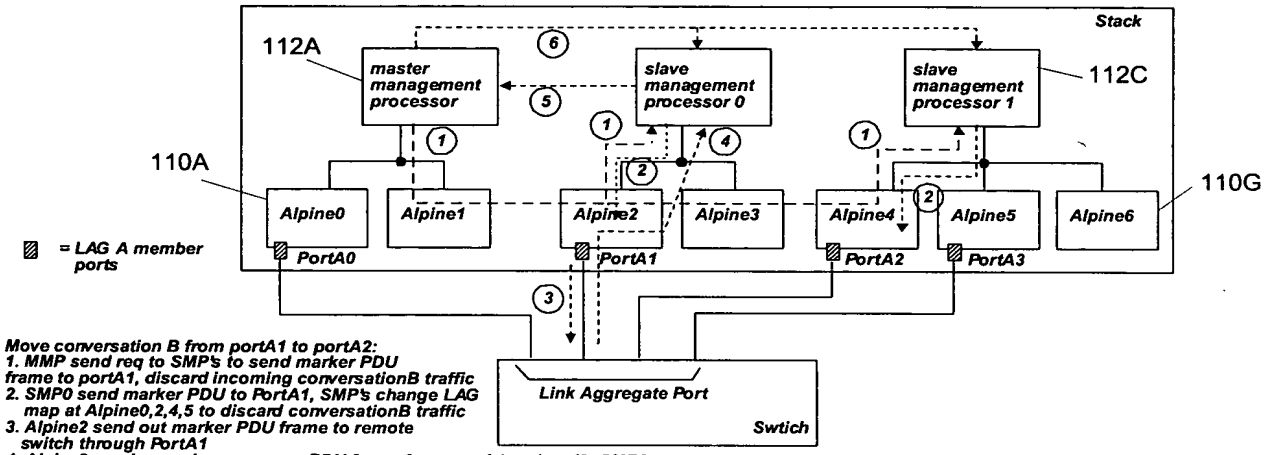
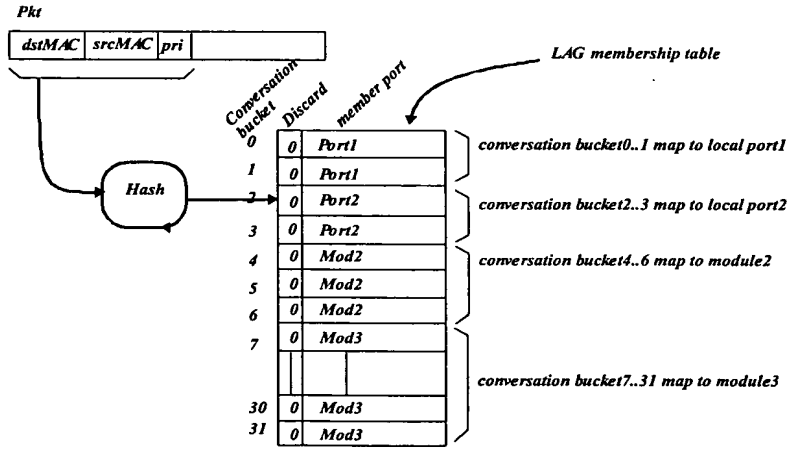


FIGURE 7



If among of traffic are same for each conversation bucket are equal then the traffic distribution will be:
Port1: will transmit 2/32 of total LAG traffic
Port2: will transmit 2/32 of total LAG traffic
Module2: will transmit 3/32 of tatal LAG traffic
Module3: will transmit 25/32 of total LAG traffic

FIGURE 8

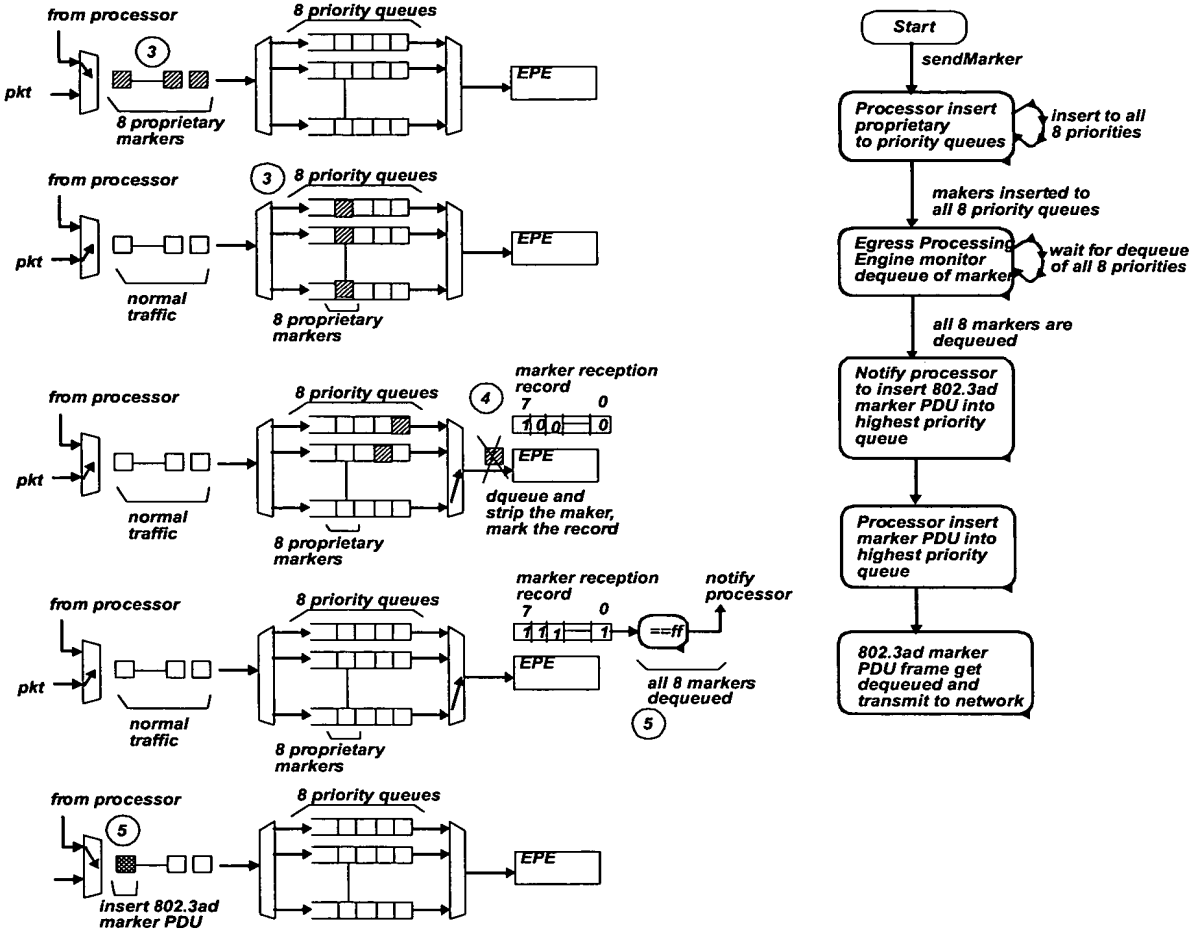

from processor
8 priority queues
pkt
3
8 proprietary markers
EPE
normal traffic
8 proprietary markers
marker reception record
7
0
4
dqueue and strip the maker, mark the record
==ff
notify processor
all 8 markers dequeued
5
insert 802.3ad marker PDU
Start
sendMarker
Processor insert proprietary to priority queues
insert to all 8 priorities
makers inserted to all 8 priority queues
Egress Processing Engine monitor dequeue of marker
wait for dequeue of all 8 priorities
all 8 markers are dequeued
Notify processor to insert 802.3ad marker PDU into highest priority queue
Processor insert marker PDU into highest priority queue
802.3ad marker PDU frame get dequeued and transmit to network

FIGURE 9